

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A record carrier comprising synchronization patterns for identifying blocks of information, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, ~~characterized in that~~ wherein the part for distinguishing the synchronization patterns consists only of the bit sequence 100 101 or of the bit sequence 010 101, and is directly followed by any 8 bit data bit sequence except the sequence 01 11 01 11.

2. (Previously Presented) Record carrier according to claim 1, characterized in that the synchronization pattern comprising a part for distinguishing the synchronization patterns consisting only of the bit sequence 100 101 or of the bit sequence 010 101, is

directly followed by a bit sequence not violating the Repeated Minimum Transition Runlength constraint.

Claim 3 (Canceled)

4. (Currently Amended) A record carrier comprising synchronization patterns for identifying blocks of information, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that wherein the part for distinguishing the synchronization patterns consists only of the bit sequence 101 001, or the bit sequence 010 100, or the bit sequence 100 100, and is directly followed by any 8 bit data bit sequence except the sequence 01 11 01 11.

5. (Currently Amended) A device for recording synchronization patterns for identifying blocks of information onto a record carrier, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that wherein the device

is operative for recording synchronization patterns comprising a part for distinguishing the synchronization patterns which consists only of the bit sequence 100 101 or of the bit sequence 010 101, and is directly followed by any 8 bit data bit sequence except the sequence 01 11 01 11.

6. (Currently Amended) A device for recording synchronization patterns for identifying blocks of information onto a record carrier, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, ~~characterized in that~~ wherein the device is operative for recording synchronization patterns comprising a part for distinguishing the synchronization patterns which consists only of the bit sequence 101 001, or the bit sequence 010 100, or the bit sequence 100 100, and is directly followed by any 8 bit data bit sequence except the sequence 01 11 01 11.

7. (Currently Amended) A device for retrieving data patterns from a record carrier, said data patterns comprising synchronization patterns for identifying blocks of information,

said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that wherein the device is operative for identifying a synchronization pattern comprising a part for distinguishing the synchronization patterns which consists only of the bit sequence 100 101 or of the bit sequence 010 101, and is directly followed by any 8 bit data bit sequence except the sequence 01 11 01 11.

8. (Currently Amended) A device for retrieving data patterns from a record carrier, said data patterns comprising synchronization patterns for identifying blocks of information, said synchronization patterns comprising a part for distinguishing one such synchronization pattern from another such synchronization pattern, characterized in that wherein the device is operative for identifying a synchronization pattern comprising a part for distinguishing the synchronization patterns which consists only of the bit sequence 101 001, or the bit sequence 010 100, or the bit sequence 100 100, and is directly followed by any 8 bit data bit sequence except the sequence 01 11 01 11.